

#2 RS

GP2851

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Miklos Sandorfi
Serial No. : 09/408,429
Filed : September 29, 1999
Title : MICROPROCESSOR INTERFACE
Art Unit : 2851
Examiner : Unknown

Assistant Commissioner for Patents
Washington, D.C. 20231



RECEIVED
DEC-2 1999
TC 2100 MAIL ROOM

INFORMATION DISCLOSURE STATEMENT

Applicant submits the references listed on the attached form PTO-1449, copies of which are enclosed.

Applicant wishes to bring to the attention of the Examiner co-pending patent applications as follows:

Serial Number	Filing Date	Entitled	Attorney Docket No.	Assigned to Art Unit
09/408,058	9/29/99	Data Storage System	07072/085001	2818
09/408,430	9/29/99	Interrupt Request Controller	07072/087001	2751
09/408,807	9/29/99	Central Processing Unit	07072/088001	2751
09/408,811	9/29/99	Error Corrector	07072/090001	2751
09/408,234	9/29/99	Fault Detector	07072/091001	2751

This statement is being filed within three months of the filing date of the application or before the receipt of a first Office action on the merits. Please apply any charges or credits to Deposit Account No. 06-1050.

RECEIVED

NOV 19 1999

TECHNOLOGY CENTER 2800

Respectfully submitted,

Richard M. Sharkansky
Reg. No. 25,800

Date: 11/9/99

Fish & Richardson P.C.
225 Franklin Street
Boston, MA 02110-2804
Telephone: (617) 542-5070
Facsimile: (617) 542-8906

CERTIFICATE OF MAILING BY FIRST CLASS MAIL
I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

November 9, 1999

Date of Deposit

Signature

Cheryl L. McGrath

Typed or Printed Name of Person Signing Certificate